

Time schedule for the planned Minisymposium

Total time: 3 Hours

- Introduction by the organizer – 10 min.
- 1. Speaker – 30 + 5 min.
Prof. Dr. S. Skelboe¹, Univ. of Copenhagen
to be announced
- 2. Speaker – 30 + 5 min.
Dipl.-Math. M. Hoschek, TU Darmstadt, and Dr. U. Feldmann, Siemens
AG München
Numerical Integration Methods for Electric Circuits Simulation Packages
- 3. Speaker – 30 + 5 min.
Dr. J. ter Maten, Philips Research Eindhoven
Numerical Methods for Frequency Domain Analysis of Electric Circuits
- 4. Speaker – 30 + 5 min.
Dr. C. Tischendorf, Humboldt-Universität zu Berlin
Asymptotical Behavior of DAEs in Circuit Simulation
- Discussion – 30 min.

List of Participants

- Dr. M. Günther and Dipl.-Math. M. Hoschek, TU Darmstadt, Fachbereich Mathematik, Schloßgartenstr. 7, 64289 Darmstadt, Germany
- Dr. E. J. W. ter Maten, Philips Research Laboratories (NatLab), Electronic Design & Tools, Analogue Sim., Prof. Holstlaan 4, Building WAY-3.73, 5656 AA Eindhoven, The Netherlands
- Prof. Dr. S. Skelboe, Department of Computer Science, DIKU, University of Copenhagen, Universitetsparken 1, 2100 Copenhagen, Denmark
- Dr. C. Tischendorf, Humboldt-Universität zu Berlin, Institut für Mathematik, Unter den Linden 6, 10099 Berlin, Germany